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10/526,818	03/03/2005	2891	900	10808/222	2	20	2

48581  
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**CONFIRMATION NO. 5473**  
**CORRECTED FILING RECEIPT**  
**\*OC000000017845557\***  
 \*OC000000017845557\*

Date Mailed: 01/13/2006

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).**

**Applicant(s)**

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**Power of Attorney:** The patent practitioners associated with Customer Number 48581.

**Domestic Priority data as claimed by applicant**

This application is a 371 of PCT/DE03/02740 08/14/2003

**Foreign Applications**

GERMANY 102 41 156.5 09/05/2002

**If Required, Foreign Filing License Granted:** 12/27/2005

**The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is US10/526,818**

**Projected Publication Date:** Not Applicable

**Non-Publication Request:** No

**Early Publication Request:** No

**Title**

Method for fabricating an integrated pin diode and associated circuit arrangement

**Preliminary Class**

438

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